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Dr. Adar Kalir received his B.Sc. (1990) and M.Sc. (1995) degrees in Industrial Engineering and Management from Tel-Aviv University, Israel. He received his Ph.D. (1999) in Industrial & Systems Engineering from Virginia Tech, USA. Prior to pursuing his Ph.D., he has held an Automation Manager position with Telrad Telecommunications and served as a Project Manager in an IE consulting firm specializing in semiconductor manufacturing. During his course of study at Virginia Tech, he has also been on a coop program with Intel. Upon completion of his Ph.D., Dr. Kalir joined Intel, where he is currently a Senior Staff Engineer on Manufacturing Technology, leading projects of productivity and throughput improvements; cost and production cycle reductions throughout Intel's factories in Israel and worldwide. In his role, he has made significant contributions to factory productivity improvements, cost reductions, and ultimately driving Intel's manufacturing to world-class cycle-time performance.

He is a member of the Phi-Kappa-Phi honorary society. He has won awards for excellence in research from the Institute of Engineers and Architects in Israel, Decision Sciences Institute, and from the IIE (2nd place doctoral dissertation award). He has published more than 20 papers in leading journals such as IIE transactions and the International Journal of Production Research and over 40 conference proceedings. Some of his work at Intel has also been published in the public domain. He serves as an Associate Editor of the IEEE on Automation Science and Engineering (T-ASE) for the 4th year in a row.